

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Sarah E. Kim et al.	Art Unit:	Unknown
Serial No.:	New Application	Examiner:	Unknown
Filed :	September 9, 2003	Assignee:	Intel Corporation
Title :	THICK METAL LAYER INTEGRATED PROCESS FLOW TO IMPROVE POWER DELIVERY AND MECHANICAL BUFFERING		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

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Respectfully submitted,



/BY
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Date: September 9, 2003

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-857001	Application No. New Application
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Sarah E. Kim et al.	
		Filing Date September 9, 2003	Group Art Unit Unknown
(37 CFR §1.98(b))		IDS filed 9/9/2003	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	E. Beyne, J. Poormans; "Enabling Technologies for Distributed and Autonomous Electronic Systems, EDAS"; imec 2002 presentation
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	